

The following documents are provided to implementors for the purpose of Ultra Ethernet [UE] Compliance self-attestation:

- UEC-Compliance-Test-Bed Recommendations.pdf- this is a best practices document describing suggestions for establishing a testbed.
- PHY-LL_Matrix_and_Checklist.xlsx - this document includes the profile matrix and compliance checklist for the PHY and Link Layer (LL) portions of the UE Specification. The PHY and LL specifications are found in UE 1.0 chapters 5 and 6.
- Transport_Matrix_and_Checklist.xlsx- this document includes the profile matrix for the Transport layer of the UE Specification, and the compliance checklist for Transport PDS. The Transport specifications are found in UE 1.0 chapter 3.

UE defines three profiles: AI Base, AI Extended, and HPC. A profile matrix states which features are mandatory, conditional, or optional for each UE profile. The profile matrix also specifies which items in the compliance checklist need to be tested if the feature or technology is implemented.

A compliance checklist contains a list of the items to test for each specification. The checklist includes the TestID, TestName, Criteria, and the reference to the specification. Each row identifies a testable item.